# CSE Terminal Masters Plan of Study in VLSI

**Name:**

**UMID:**

**Advisor (signature required):**

**Date:**

**MS Degree** or **MSE Degree** (circle one)

(What is your undergrad degree field? **Engineering** or **Non-engineering** (circle one)

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<tr>
<th>Degree Term:</th>
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## CSE Courses

- **500 level or above:** at least 15 credit hours
- **427 and 627: both are required (VLSI Kernel Requirements)**
- **Two 400-level: 482, 483, 484, 485, 487, 489, 584, 587:**
- **485, 571, 582, 584, 587:**
- **571, 583, 584, 587:**
- **588, 591:**
- **542, 543, 545, 567, 576, or 592:**
- **542, 543, 545, 567, 576, or 592:**
- **574, 575, or 586:**

## Technical Electives

- **at least 24 credit hours**

## CSE Courses 500 level or above:

- **at least 30 credit hours**

## Total Hours:

(fill in for each column)

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<tr>
<th>Term</th>
<th>Course</th>
<th>Grade</th>
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### NOTES:

1. A maximum of six credit hours of individual study, research, and seminars
2. You must meet all Rackham and Program requirements (see brochures for details)
3. It is expected that most entering students will have already completed courses equivalent to (482 or 483) and 492 and (470 or 478)
4. Seminar, directed study credits (except 3 hrs. of EECS 599) do not count toward the 500 level course requirement
5. It is the student’s responsibility to see that all requirements are met.
6. You must choose 2 of the 4 areas, in addition to the VLSI Kernel
7. One of the 500-level must be from the approved course list at the end of the CSE Graduate Program Guide
8. If you already have a master’s degree that is deemed relevant by CSE, you are not eligible for a master’s degree from this program.

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**for office use only:**

- No grades below B-
- Approved MPS
- Other, Masters thesis, TC

**GPA** **CTP**

**Term:**

08/20